



Estd: 2001

## NARAYANA ENGINEERING COLLEGE :: GUDUR

(Approved by AICTE, Affiliated to JNTUA & An ISO 9001-2015 Certified Organization)

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

### Guest Lecture

### Report on “ELECTRONIC DEVICES&CIRCUITS”

**DATE: 25.09.2019**

The Department of EEE has organized a guest lecture on "**ELECTRONIC DEVICES&CIRCUITS**" under IEI ECSC by **Mr.S.MM TRINATH, Expert Faculty from ACE Engineering Academy, Hyderabad on 25<sup>th</sup> September 2019** at EEE seminar Hall in between 12:00 PM to 2:00 PM .



**Mr.S.MM TRINATH** emphasized on overview of **EDC** design flow including from the design specifications to fabrication of chip and bought it into market. He discussed about design specifications like block diagram of main and individual modules, no.of. inputs and outputs of all module, speed, power dissipation, package types and cost. The lecture also discussed about simulation process like logical verification of modules, sub modules in terms of truth tables and waveforms to check functionality.



The lecture has also provided information on system partitioning, floor planning such as estimation of required chip area that will be used for standard cells and determine shapes of sizes of each sub module to get high performance. The lecture also focused on placement of each module, routing and testing techniques such as design & electrical rule check, short circuit & antenna test and troubleshooting of various faults on IC's. Around 48 students of second year B.Tech of EEE has attended and interacted with the guest. A good feedback has been given by the students.



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